

CONTROLLER #1
MISCELLANEOUS PIN ASSIGNMENTS

CONN	PIN	SIGNAL LABEL	TO	TO	DESCRIPTION
J2	5	ELO	SW1	-	MOTOR LOCKOUT SWITCH ON RIGHT SIDE PANEL
J2	6	LSCOM	ISO5V	-	LIMIT SWITCH COMMON
J2	11	OPWR	+24V	-	POWER FOR HI-PWR OUTPUTS (DO 1-16)
J2	14	ORET	24V RTN	-	RETURN FOR ABOVE(24V SUPPLY)
J2	15	5V OUT	-	-	NOT USED
J2	17	INCOM	ISO5V	-	COMMON FOR ISOLATED INPUTS (LOGIC SUPPLY)
J2	30	5V OUT	-	-	NOT USED
J2	31	-	-	-	-
J2	36	-	-	-	-
J2	40	-	-	-	-
J3	1	AGND	-	-	ANALOG GROUND
J3	2	AN1	-	-	ANALOG INPUT 1
J3	3	AN3	-	-	ANALOG INPUT 3
J3	6	AGND	-	-	ANALOG GROUND
J3	7	-12V	-	-	-12V FROM CONTROLLER
J3	8	+5V IN	-	-	-
J3	9	AGND	-	-	ANALOG GROUND
J3	10	AN2	-	-	ANALOG INPUT 2
J3	15	+12V	-	-	+12V FROM CONTROLLER
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-

CONTROLLER #2
MISCELLANEOUS PIN ASSIGNMENTS

CONN	PIN	SIGNAL LABEL	TO	TO	DESCRIPTION
J2	5	ELO	SW1	-	MOTOR LOCKOUT SWITCH ON RIGHT SIDE PANEL
J2	6	LSCOM	ISO5V	-	LIMIT SWITCH COMMON
J2	11	OPWR	+24V	-	POWER FOR HI-PWR OUTPUTS (DO 1-16)
J2	14	ORET	24V RTN	-	RETURN FOR ABOVE(24V SUPPLY)
J2	15	5V OUT	-	-	NOT USED
J2	17	INCOM	ISO5V	-	COMMON FOR ISOLATED INPUTS (LOGIC SUPPLY)
J2	30	5V OUT	-	-	NOT USED
J2	31	-	-	-	-
J2	36	-	-	-	-
J2	40	-	-	-	-
J3	1	AGND	-	-	ANALOG GROUND
J3	2	AN1	-	-	ANALOG INPUT 1
J3	3	AN3	-	-	ANALOG INPUT 3
J3	6	AGND	-	-	ANALOG GROUND
J3	7	-12V	-	-	-12V FROM CONTROLLER
J3	8	+5V IN	-	-	-
J3	9	AGND	-	-	ANALOG GROUND
J3	10	AN2	-	-	ANALOG INPUT 2
J3	15	+12V	-	-	+12V FROM CONTROLLER
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-

B

REVISIONS

UNIVERSITY OF CALIFORNIA LICK OBSERVATORY		CONTROLLER MISC WIRING KAST SPECTROGRAPH	
DES'N BY: B. Alcott	ORIGIN DATE: 11-16-07	DWG. NO.	NUM. 1 OF 1
Last SN	MODIFY DATE:	EL-3705	REV. A
PRINT TIME: 07:49:41	PRINT DATE: Fri Jan 09, 2009		
PATH: KAST\		Kast_misc_controller_wiring.sch	