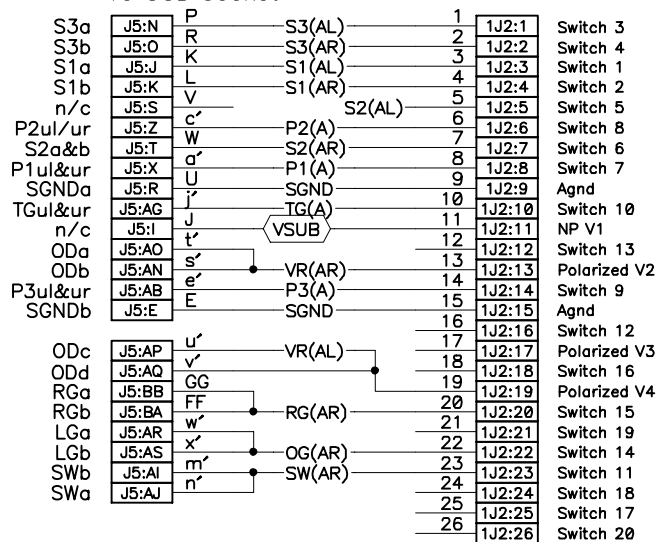


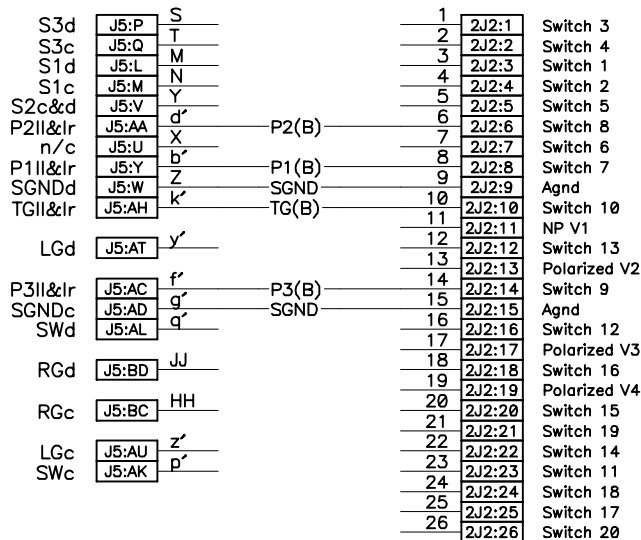
J5 PT06A(SR)-24-61S

To CCD socket



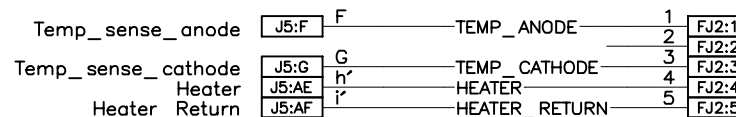
J5 PT06A(SR)-24-61S

To CCD socket



J5 PT06A(SR)-24-61S

To CCD socket

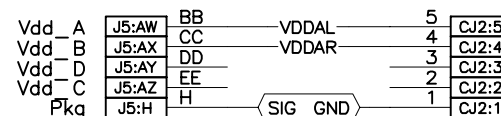


Power Filter Card

J2 Temp/Heater Connector

J5 PT06A(SR)-24-61S

To CCD socket

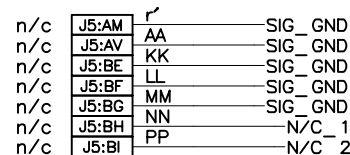
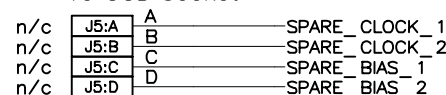


10 K uF Card

J2 output connector

J5 PT06A(SR)-24-61S

To CCD socket



REVISION

07-31-97: Moved wires to reflect wiring changes.  
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UNIVERSITY OF CALIFORNIA  
 LICK OBSERVATORY

ELECTRONICS BOX CABLING  
 PRIME FOCUS CAMERA

DES'N BY: L. Bresee

ORIGIN DATE: 09-23-96

DWG. NO.

NUM. 1 OF 4

DRAWN BY:

MODIFY DATE: 08-26-97

PATH: PFCAM\DEWAR\EBOXWIRE

REV. B

EL-3291

P7 PT02A-24-61P  
From SDSU Controller

P7:X	a'	SPARE3-1
P7:Z	c'	SPARE5-1
P7:AB	e'	SPARE5-2
P7:AD	g'	SPARE17-2
P7:AF	i'	SPARE18-0
P7:AH	k'	SPARE18-1
P7:AJ	n'	SPARE6-0
P7:AL	q'	SPARE6-1

P7 PT02A-24-61P  
From SDSU Controller

P7:AC	f'	S1(AL)-IN	1	1J1:1
P7:AA	d'	S1(AR)-IN	2	1J1:2
P7:AS	x'	S3(AL)-IN	3	1J1:3
P7:AQ	v'	S3(AR)-IN	4	1J1:4
P7:AK	p'	S2(AL)-IN	5	1J1:5
P7:AI	m'	S2(AR)-IN	6	1J1:6
P7:AY	DD	P1(A)-IN	7	1J1:7
P7:BC	HH	P2(A)-IN	8	1J1:8
P7:BG	MM	P3(A)-IN	9	1J1:9
P7:BD	JJ	TG(A)-IN	10	1J1:10
P7:AV	AA	SW(AR)-IN	11	1J1:11
P7:AX	CC	SW(AL)-IN	12	1J1:12
P7:S	V	OG(AL)-IN	13	1J1:13
P7:Q	T	OG(AR)-IN	14	1J1:14
P7:AN	s'	RG(AR)-IN	15	1J1:15
P7:AP	u'	RG(AL)-IN	16	1J1:16

J1 input to  
Analog Switch Card #1

S1	17	1J1:17
S2	18	1J1:18
S3	19	1J1:19
S4	20	1J1:20
S5	21	1J1:21
S6	22	1J1:22
S7	23	1J1:23
S8	24	1J1:24
S9	25	1J1:25
S10	26	1J1:26
S11	27	1J1:27
S12	28	1J1:28
S13	29	1J1:29
S14	30	1J1:30
S15	31	1J1:31
S16	32	1J1:32
S17	33	1J1:33
S18	34	1J1:34
S19	35	1J1:35
S20	36	1J1:36
Sig_GND	37	1J1:37
Sig_GND	38	1J1:38
Sig_GND	39	1J1:39
Sig_GND	40	1J1:40
NP-V1		
Dgnd		
V2		
ID32		
V3		
ID16		
V4		
ID8		
Venable		
ID4		
Agnd		
ID2		
-15Vdc		
ID1		
+15Vdc		
Vcc		

Power Filter  
Card J3

FJ3:11 11 DGND

Power Filter  
Card J3

P7:D	D	SGND	21	1J1:21
P7:H	H	SGND	22	1J1:22
P7:L	M	SGND	23	1J1:23
P7:P	S	SGND	24	1J1:24
P7:Y	b'	VSUB	25	1J1:25

P7:J J VR(AR)-IN 27 1J1:27

P7:K L VR(AL)-IN 31 1J1:31

FJ3:8	8	VENABLE	33	1J1:33
FJ3:3	3		34	1J1:34
FJ3:4	4	AGND	35	1J1:35
FJ3:2	2		36	1J1:36
FJ3:1	1	-15VDC	37	1J1:37
FJ3:10	10		38	1J1:38
FJ3:5	5	+15VDC	39	1J1:39
FJ3:9	9	+5VDC	40	1J1:40
FJ3:6	6			
FJ3:7	7			

P7:A A SPARE3-0 Reserved for shields

P7 PT02A-24-61P  
From SDSU Controller

P7:C	C	VDDAL-IN	1	CJ1:1
P7:V	Y	VDDAR-IN	2	CJ1:2
P7:G	G	VDDBL-IN	3	CJ1:3
P7:E	E	VDDBR-IN	4	CJ1:4
P7:B	B	SIG_GND	5	CJ1:5

Power Filter  
Card J4

FJ4:11 11 DGND

Power Filter  
Card J4

P7:F	F	SGND	21	2J1:21
P7:J	K	SGND	22	2J1:22
P7:N	P	SGND	23	2J1:23
P7:R	U	SGND	24	2J1:24
		VSUB	25	2J1:25

P7:M N VR(BR)-IN 27 2J1:27

P7:O R VR(BL)-IN 31 2J1:31

FJ4:8	8	VENABLE	33	2J1:33
FJ4:3	3		34	2J1:34
FJ4:4	4	AGND	35	2J1:35
FJ4:2	2		36	2J1:36
FJ4:1	1	-15VDC	37	2J1:37
FJ4:10	10		38	2J1:38
FJ4:5	5	+15VDC	39	2J1:39
FJ4:9	9	+5VDC	40	2J1:40
FJ4:6	6			
FJ4:7	7			

P7 PT02A-24-61P  
From SDSU Controller

P7:BH	NN	DCRESTORE	3	FJ7:3
P7:T	W	SIG_GND	2	FJ7:2
			1	FJ7:1

P7 PT02A-24-61P  
From SDSU Controller

P7:AG	j'	S1(BL)-IN	1	2J1:1
P7:AE	h'	S1(BR)-IN	2	2J1:2
P7:AW	BB	S3(BL)-IN	3	2J1:3
P7:AU	z'	S3(BR)-IN	4	2J1:4
P7:AQ	t'	S2(BL)-IN	5	2J1:5
P7:AO	r'	S2(BR)-IN	6	2J1:6
P7:AM	FF	P1(B)-IN	7	2J1:7
P7:BA	KK	P2(B)-IN	8	2J1:8
P7:BE	PP	P3(B)-IN	9	2J1:9
P7:BI	LL	TG(B)-IN	10	2J1:10
P7:AZ	EE	SW(BR)-IN	11	2J1:11
P7:BB	GG	SW(BL)-IN	12	2J1:12
P7:W	Z	OG(BL)-IN	13	2J1:13
P7:U	X	OG(BR)-IN	14	2J1:14
P7:AR	w'	RG(BR)-IN	15	2J1:15
P7:AT	y'	RG(BL)-IN	16	2J1:16

J1 input to  
Analog Switch Card #2

S1	17	2J1:17
S2	18	2J1:18
S3	19	2J1:19
S4	20	2J1:20
S5	21	2J1:21
S6	22	2J1:22
S7	23	2J1:23
S8	24	2J1:24
S9	25	2J1:25
S10	26	2J1:26
S11	27	2J1:27
S12	28	2J1:28
S13	29	2J1:29
S14	30	2J1:30
S15	31	2J1:31
S16	32	2J1:32
S17	33	2J1:33
S18	34	2J1:34
S19	35	2J1:35
S20	36	2J1:36
Sig_GND	37	2J1:37
Sig_GND	38	2J1:38
Sig_GND	39	2J1:39
Sig_GND	40	2J1:40
NP-V1		
Dgnd		
V2		
ID32		
V3		
ID16		
V4		
ID8		
Venable		
ID4		
Agnd		
ID2		
-15Vdc		
ID1		
+15Vdc		
Vcc		

Power Filter Card  
J7

UNIVERSITY OF CALIFORNIA  
LICK OBSERVATORY

ELECTRONICS BOX CABLING  
PRIME FOCUS CAMERA

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DES'N BY: L. Bresee

ORIGIN DATE: 09-23-96

DWG. NO.

NUM. 2 OF 4

DRAWN BY:

MODIFY DATE: 08-26-97

PATH: PFCAM\DEWAR\EBOXWIRE

REV. B

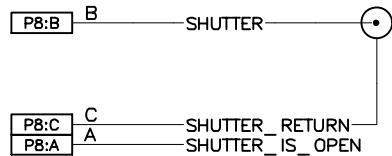
EL-3291

P8 MS3103A-20-29  
From SDSU Controller

Power Filter Card  
J1 Input

P8:G	G	-15VDC	1	FJ1:1
P8:J	K	TEMP_ANODE	2	FJ1:2
P8:H	H	AGND	3	FJ1:3
			4	FJ1:4
P8:F	F	+15VDC	5	FJ1:5
P8:K	L	TEMP_CATHODE	6	FJ1:6
P8:E	E	AGND	7	FJ1:7
P8:I	J	VENABLE	8	FJ1:8
P8:P	S	+5VDC	9	FJ1:9
P8:L	M	HEATER	10	FJ1:10
P8:O	R	DGND	11	FJ1:11
P8:M	N	HEATER_RETURN	12	FJ1:12
P8:D	D	SPARE(30VDC)		
P8:N	P	SPARE(AGND)		
P8:Q	T	SPARE		

P13



Power Filter  
Card J5

AMP 1  
J1

FJ5:1	1	AGND	1	A1J1:1
FJ5:2	2	-15VDC	2	A1J1:2
FJ5:3	3	+15VDC	3	A1J1:3
FJ5:4	4	AGND	4	A1J1:4
FJ5:5	5		5	A1J1:5
FJ5:6	6	CLAMP_CATHODE	6	A1J1:6
FJ5:7	7	CLAMP_ANODE	7	A1J1:7

FJ6:1	1	AGND	1	A2J1:1
FJ6:2	2	-15VDC	2	A2J1:2
FJ6:3	3	+15VDC	3	A2J1:3
FJ6:4	4	AGND	4	A2J1:4
FJ6:5	5		5	A2J1:5
FJ6:6	6	CLAMP_CATHODE	6	A2J1:6
FJ6:7	7	CLAMP_ANODE	7	A2J1:7

Power Filter  
Card J6

AMP 2  
J1

UNIVERSITY OF CALIFORNIA  
LICK OBSERVATORY

ELECTRONICS BOX CABLING  
PRIME FOCUS CAMERA

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DES'N BY: L. Bresee

ORIGIN DATE: 09-23-96

DWG. NO.

NUM. 3 OF 4

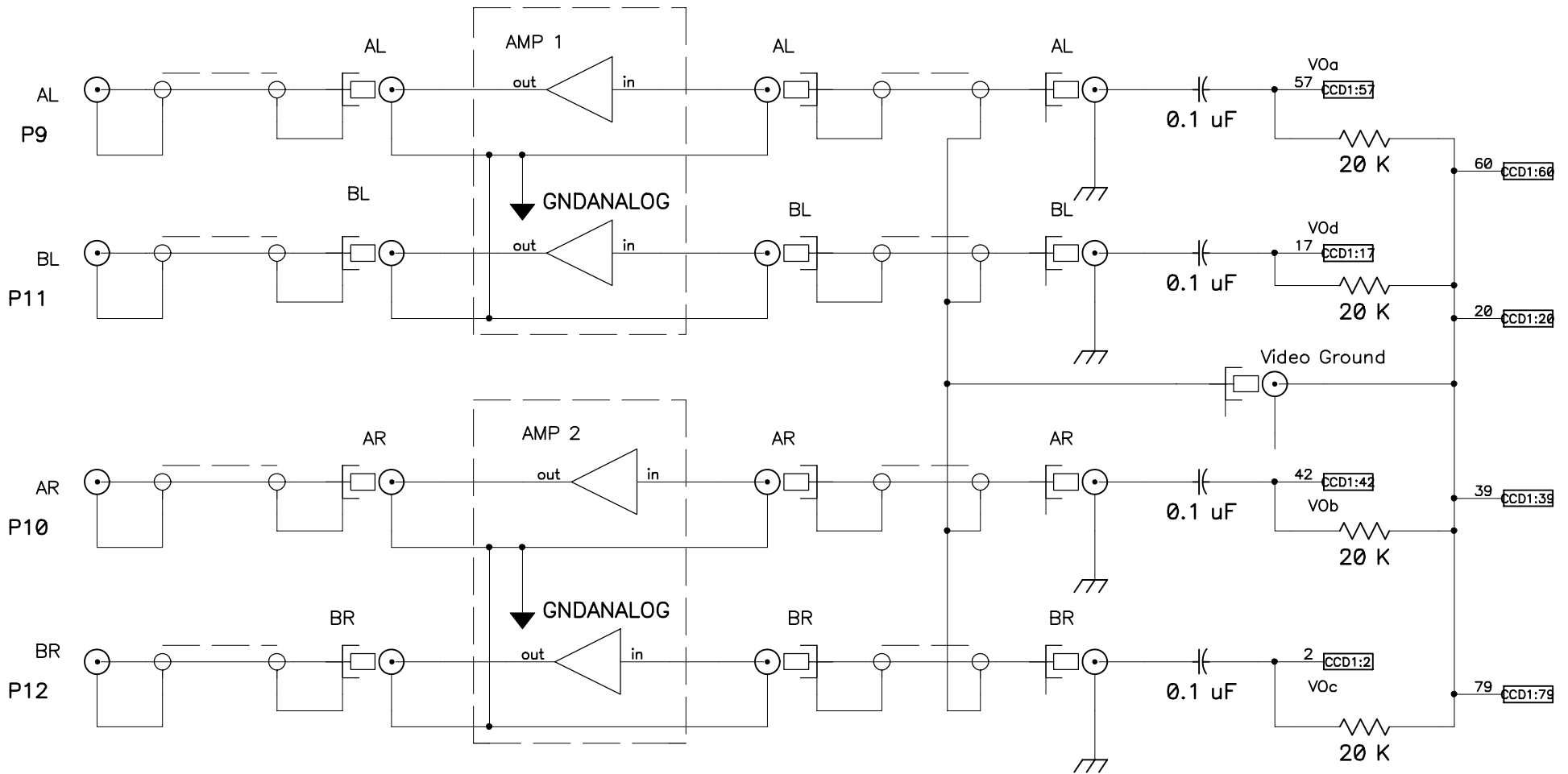
DRAWN BY:

MODIFY DATE: 08-26-97

PATH: PFCAM\DEWAR\EBOXWIRE

REV. B

EL-3291



Video signal coax cables have shields wired to center of SMA coax connector "Video Ground," and not to SMA connector cases. The coax shields connect the video ground to AGND via the SMB coax connectors on the preamplifier cards.

<b>REVISION</b>		<b>UNIVERSITY OF CALIFORNIA LICK OBSERVATORY</b>		<b>ELECTRONICS BOX CABLING PRIME FOCUS CAMERA</b>	
07-31-97: Moved wires to reflect wiring changes. 08-20-97: Added 4 SGND wired to CCD connector, swapped pin A and Y on SDSU connector.		DES'N BY: <b>L. Bresee</b>	ORIGIN DATE: <b>09-23-96</b>	DWG. NO.	NUM. 4 OF 4
08-26-97: Changed connector numbering 01-28-98: Unknown grounding changes per K. Gilmore to connect SGND in electronics box.		DRAWN BY:	MODIFY DATE: <b>08-26-97</b>	<b>EL-3291</b>	
		PATH: <b>PFCAM\DEWAR\EBOXWIRE</b>	REV. <b>B</b>		