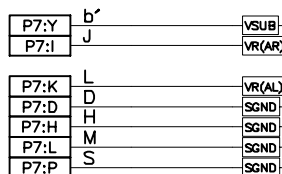
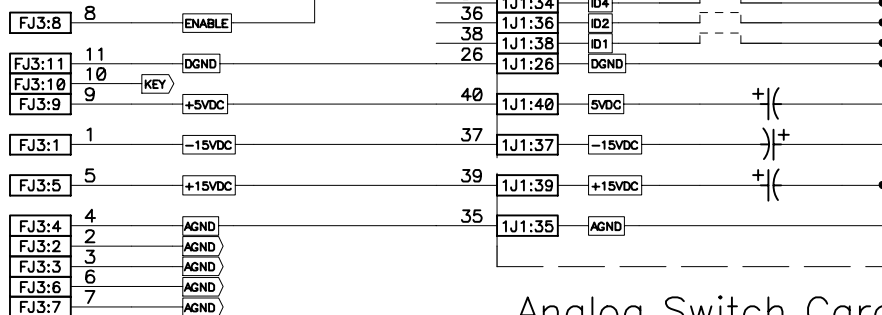


P7 PT02A-24-61P  
From SDSU Controller



J5 PT06A(SR)-24-61S  
To CCD socket



Analog Switch Card #1

Power Filter Card  
11 pin molex J3  
Analog Switch Card  
Supply Connector

UNIVERSITY OF CALIFORNIA  
LICK OBSERVATORY

ELECTRONICS BOX WIRING  
ANALOG SWITCH CARD SECTION  
PRIME FOCUS CAMERA

REVISION

07-29-97: Modified to reflect actual wiring and new changes.  
08-04-97: Changed parallel clock labels and added CCD pin labels  
08-20-97: Added 4 SGND wires to CCD socket, swapped pins A and Y  
08-26-97: Changed connector numbering  
01-24-98: Unknown modifications made by K. Gilmore, probably  
to the effect of connecting SGND locations inside the electronics box.

DES'N BY: L. Bresee

ORIGIN DATE: 03-31-97

DWG. NO. NUM. 1 OF 4

DRAWN BY:

MODIFY DATE: 08-26-97

PATH: PFCAM\DEWAR\EBOX

REV. D

EL-3290

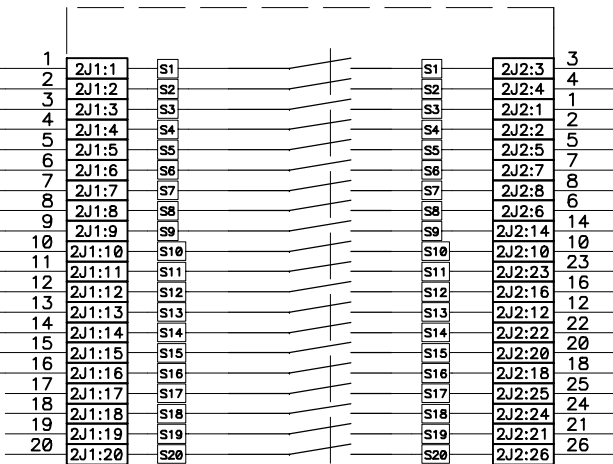
P7:AG	i'	S1(BL)
P7:AE	h'	S1(BR)
P7:AW	BB	S3(BL)
P7:AU	z'	S3(BR)
P7:AO	t'	S2(BL)
P7:AM	r'	S2(BR)
P7:BA	FF	P1(B)
P7:BE	KK	P2(B)
P7:BI	PP	P3(B)
P7:BF	LL	TG(B)
P7:AZ	EE	SW(BR)
P7:BB	GG	SW(BL)
P7:W	Z	OG(BL)
P7:U	X	OG(BR)
P7:AR	w'	RG(BR)
P7:AT	y'	RG(BL)

P7 PT02A-24-61P  
From SDSU Controller

P7:M	N	VR(BR)
P7:O	R	VR(BL)
P7:F	F	SGND
P7:J	K	SGND
P7:N	P	SGND
P7:R	U	SGND

FJ4:8	8	ENABLE
FJ4:11	11	DGND
FJ4:10	10	KEY
FJ4:9	9	+5VDC
FJ4:1	1	-15VDC
FJ4:5	5	+15VDC
FJ4:4	4	AGND
FJ4:2	2	AGND
FJ4:3	3	AGND
FJ4:6	6	AGND
FJ4:7	7	AGND

Power Filter Card  
11 pin molex J4  
Analog Switch Card  
Supply Connector



M	J5:L	S1(D)
N	J5:M	S1(C)
S	J5:P	S3(D)
T	J5:Q	S3(C)
b'	J5:Y	P1(I&I'r)
d'	J5:AA	P2(I&I'r)
f'	J5:AC	P3(I&I'r)
k'	J5:AH	TG(I&I'r)
q'	J5:AL	SW(D)
p'	J5:AK	SW(C)
y'	J5:AT	LG(D)
z'	J5:AU	LG(C)
HH	J5:BC	RG(C)
JJ	J5:BD	RG(D)

P1(B)
P2(B)
P3(B)
TG(B)

SGND
SGND

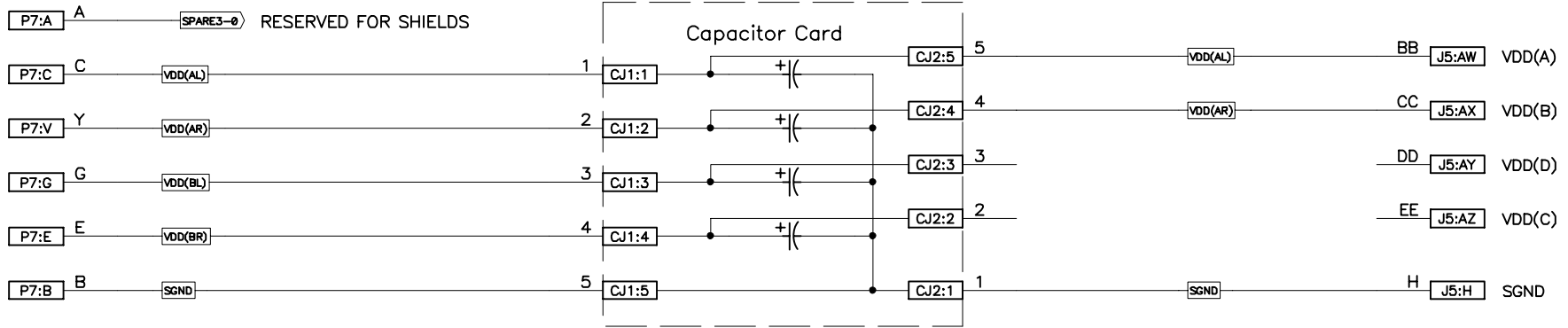
J5 PT06A(SR)-24-61S  
To CCD socket

\*1: Two wires connected to P1-b'.  
One runs to each analog switch card.

Analog Switch Card #2

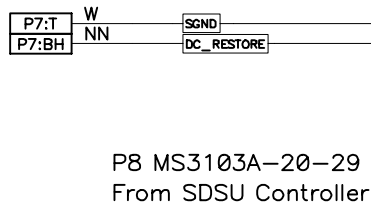
UNIVERSITY OF CALIFORNIA LICK OBSERVATORY		ELECTRONICS BOX WIRING ANALOG SWITCH CARD SECTION PRIME FOCUS CAMERA	
DES'N BY: L. Bresee	ORIGIN DATE: 03-31-97	DWG. NO.	NUM. 2 OF 4
DRAWN BY:	MODIFY DATE: 01-24-98	<b>EL-3290</b>	
PATH: PFCAM\DEWAR\EBOX			

REVISION	
07-29-97:	Modified to reflect actual wiring and new changes.
08-04-97:	Changed parallel clock labels and added CCD pin labels
08-20-97:	Added 4 SGND wires to CCD socket, swapped pins A and Y
08-26-97:	Changed connector numbering
01-24-98:	Unknown modifications made by K. Gilmore, probably to the effect of connecting SGND locations inside the electronics box.

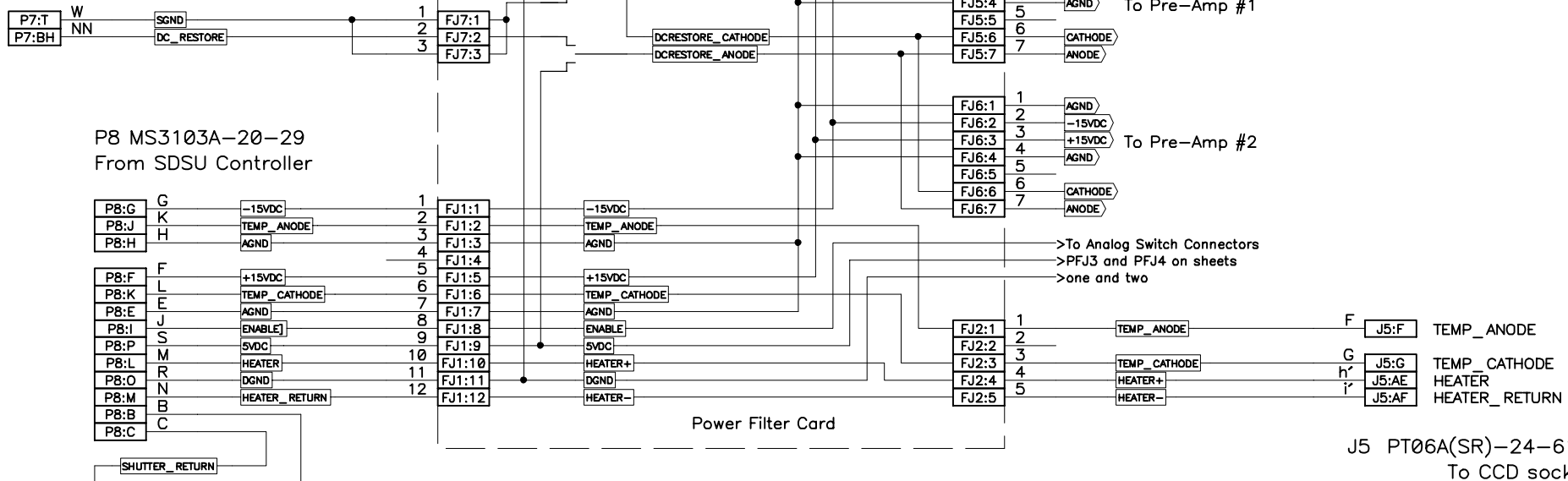


P7 PT02A-24-61P  
From SDSU Controller

J5 PT06A(SR)-24-61S  
To CCD socket



P8 MS3103A-20-29  
From SDSU Controller



Power Filter Card

J5 PT06A(SR)-24-61S  
To CCD socket

P13

REVISION

07-29-97: Modified to reflect actual wiring and new changes.  
 08-04-97: Changed parallel clock labels and added CCD pin labels  
 08-20-97: Added 4 SGND wires to CCD socket, swapped pins A and Y  
 08-26-97: Changed connector numbering  
 01-24-98: Unknown modifications made by K. Gilmore, probably  
 to the effect of connecting SGND locations inside the electronics box.

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LICK OBSERVATORY

ELECTRONICS BOX WIRING  
POWER FILTER CARD SECTION  
PRIME FOCUS CAMERA

DES'N BY: L. Bresee

ORIGIN DATE: 03-31-97

DWG. NO.

NUM. 3 OF 4

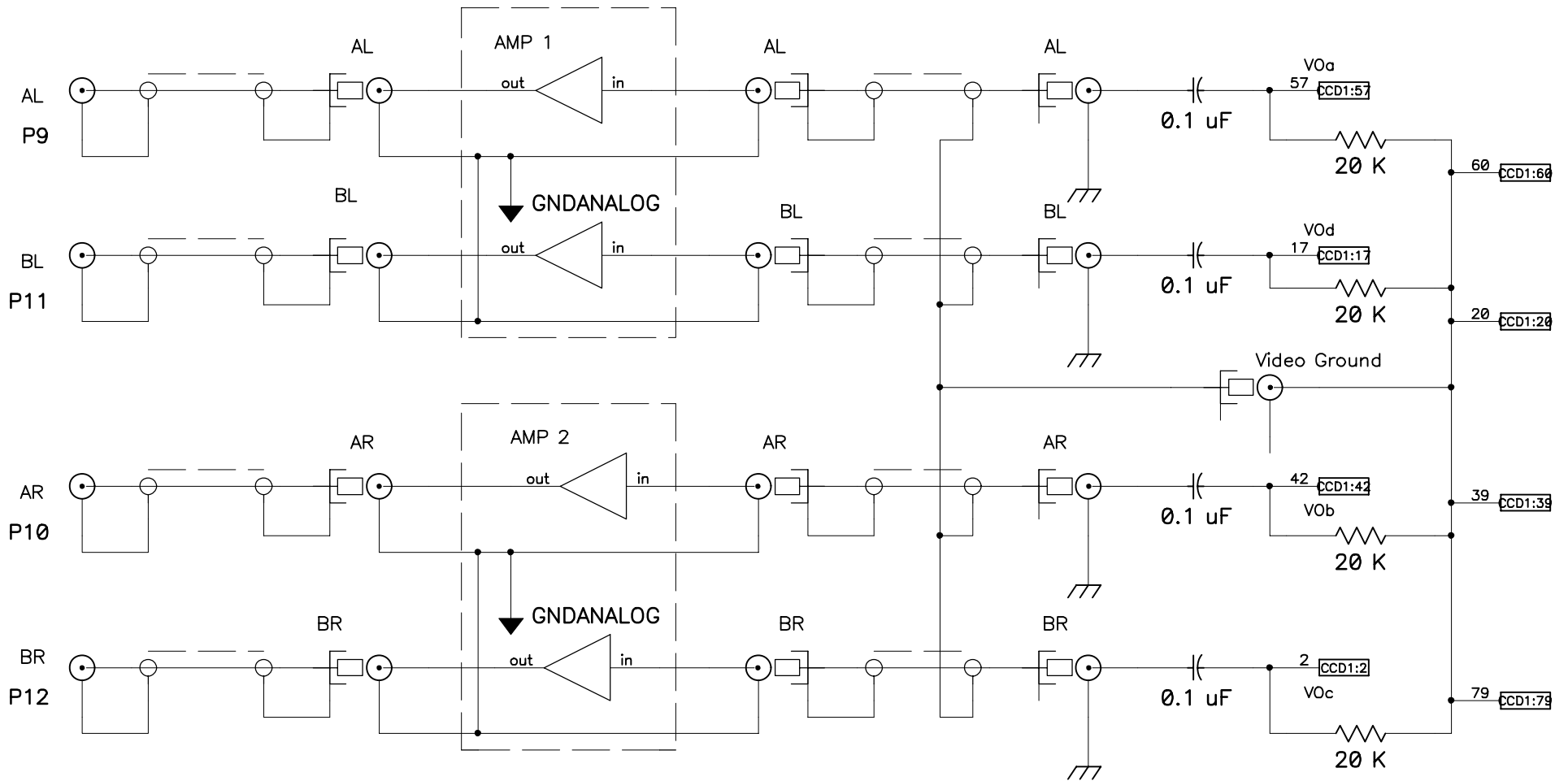
DRAWN BY:

MODIFY DATE: 01-24-98

PATH: PFCAM\DEWAR\EBOX

REV. D

EL-3290



Prime Focus Camera  
Electronics Box Wiring

Video signal coax cables have shields wired to center of SMA coax connector "Video Ground," and not to SMA connector cases. The coax shields connect the video ground to AGND via the SMB coax connectors on the preamplifier cards.

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ELECTRONICS BOX WIRING  
VIDEO GROUNDING SECTION  
PRIME FOCUS CAMERA

REVISION

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01-24-98: Unknown modifications made by K. Gilmore, probably  
to the effect of connecting SGND locations inside the electronics box.

DES'N BY: L. Bresee

ORIGIN DATE: 03-31-97

DWG. NO.

NUM. 4 OF 4

DRAWN BY:

MODIFY DATE: 01-24-98

PATH: PFCAM\DEWAR\EBOX

REV. D

EL-3290