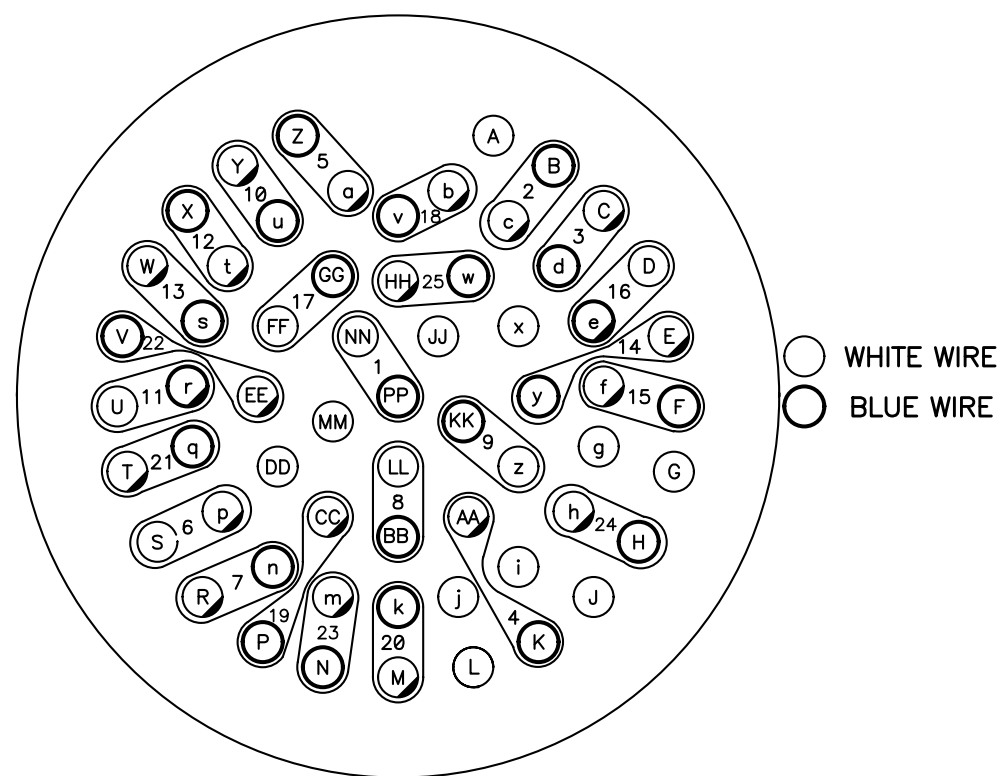


PT06A-24-61S(SR)



UNCONNECTED PINS ON 61 PIN PT06A-24-61S(SR)

GND	g'	7
n.c. (VDD(B) on EL-3192)	G	30
GND	i'	9
n.c. (P3-B on EL-3192)	J	32
n.c. (P1-B on EL-3192)	j'	11
GND	L	33
n.c. open pair (SPARE4 on EL-3192)	JJ	45
n.c. open pair (SPARE4 RTN on EL-3192)	x'	56
n.c. open pair (SPARE3 on EL-3192)	MM	51
n.c. open pair (SPARE3 RTN on EL-3192)	DD	59

UNIVERSITY OF CALIFORNIA  
LICK OBSERVATORY

Front Panel Interconnect 61 Pin MS  
to 51 Micro-D Dewar Electronics Box  
LRIS Upgrade

REVISIONS

schematics\LRIS\_FPI-61\_EBox-51\_sh\_3 .pdf

DES'N BY: M. L. SAYLOR

ORIGIN DATE: 4/7/09

DWG. NO.

NUM. 3 OF 3

Last SN

MODIFY DATE:

PRINT TIME: 11:43:36

PRINT DATE: Thu Apr 09, 2009

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