

Revision	Date	Who	ECN #	Description of Change(s)
1.0	9-Apr-2009	MP	-	Initial version.
1.1	20-Apr-2009	MP	-	Added decoupling caps on U25, U27, connected floating nets, released to fabrication
1.2	22-Apr-2009	MP	-	OPEN_POWER on Sheet 5, added R56 input to unused gate (polarity correction on Acopian output footprint)



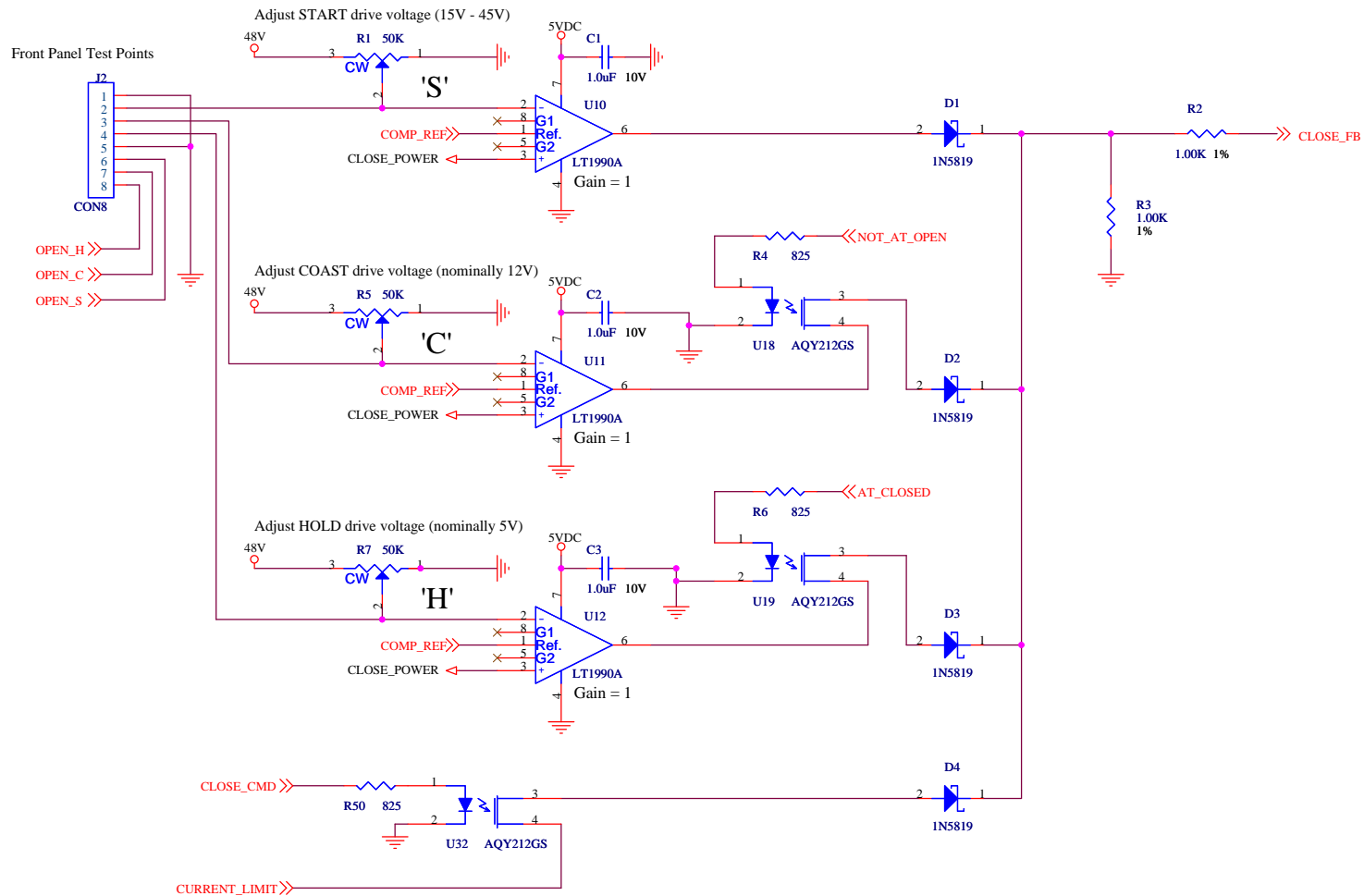
University of California, Lick Observatory
 1156 High Street
 Santa Cruz, CA 95064

Schematic, PCB, LRIS Shutter Driver

Size B	CAGE Code	DWG NO EL-4222	Rev 1.2
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Wednesday, April 22, 2009	Scale	Sheet 1 of 6
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CLOSE DRIVE SETTINGS



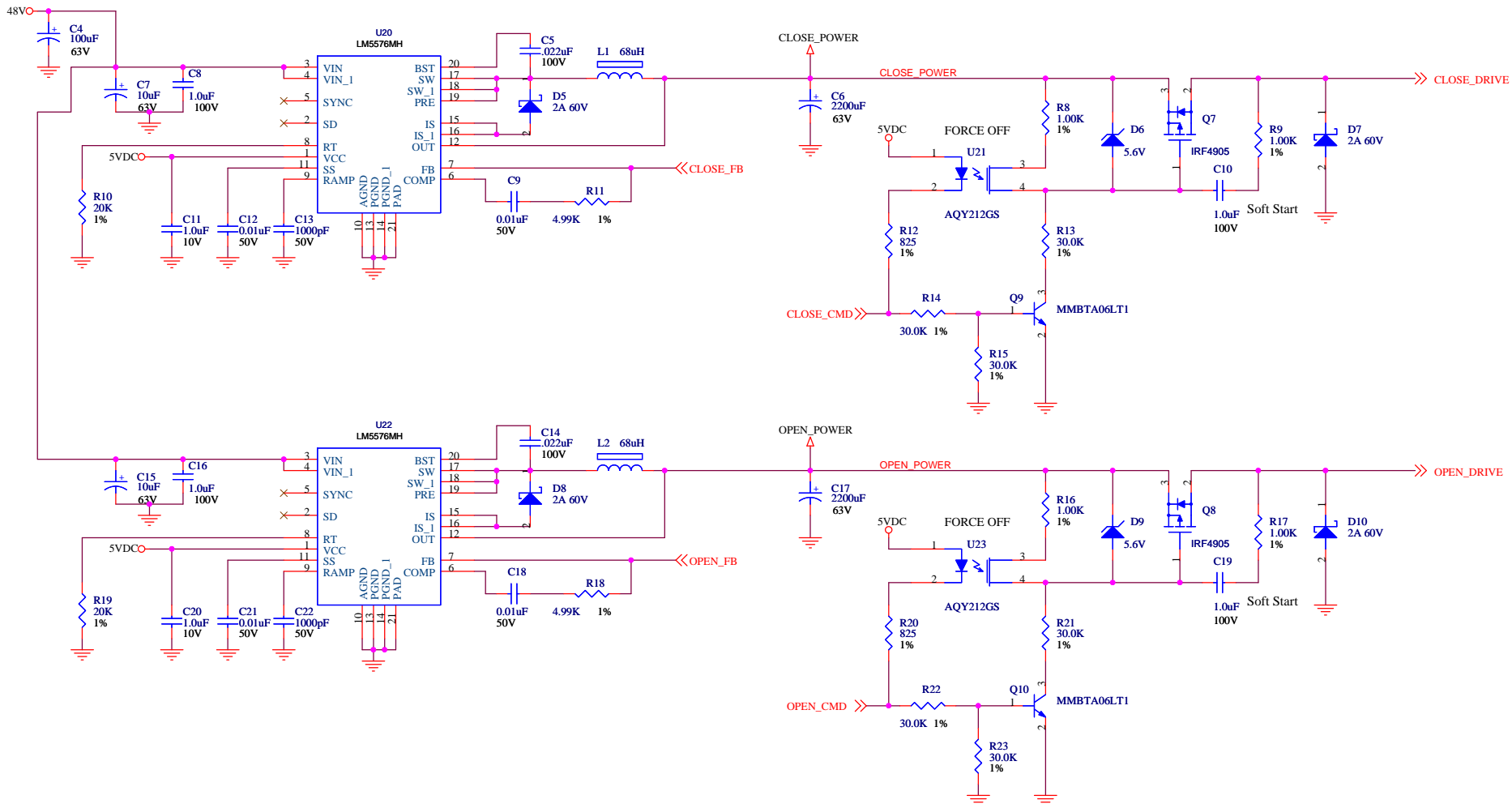
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Schematic, PCB, LRIS Shutter Driver
Close Drive Settings

Size B	CAGE Code	DWG NO EL-4222	Rev 1.2
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Wednesday, April 22, 2009

Scale	Sheet 2 of 6
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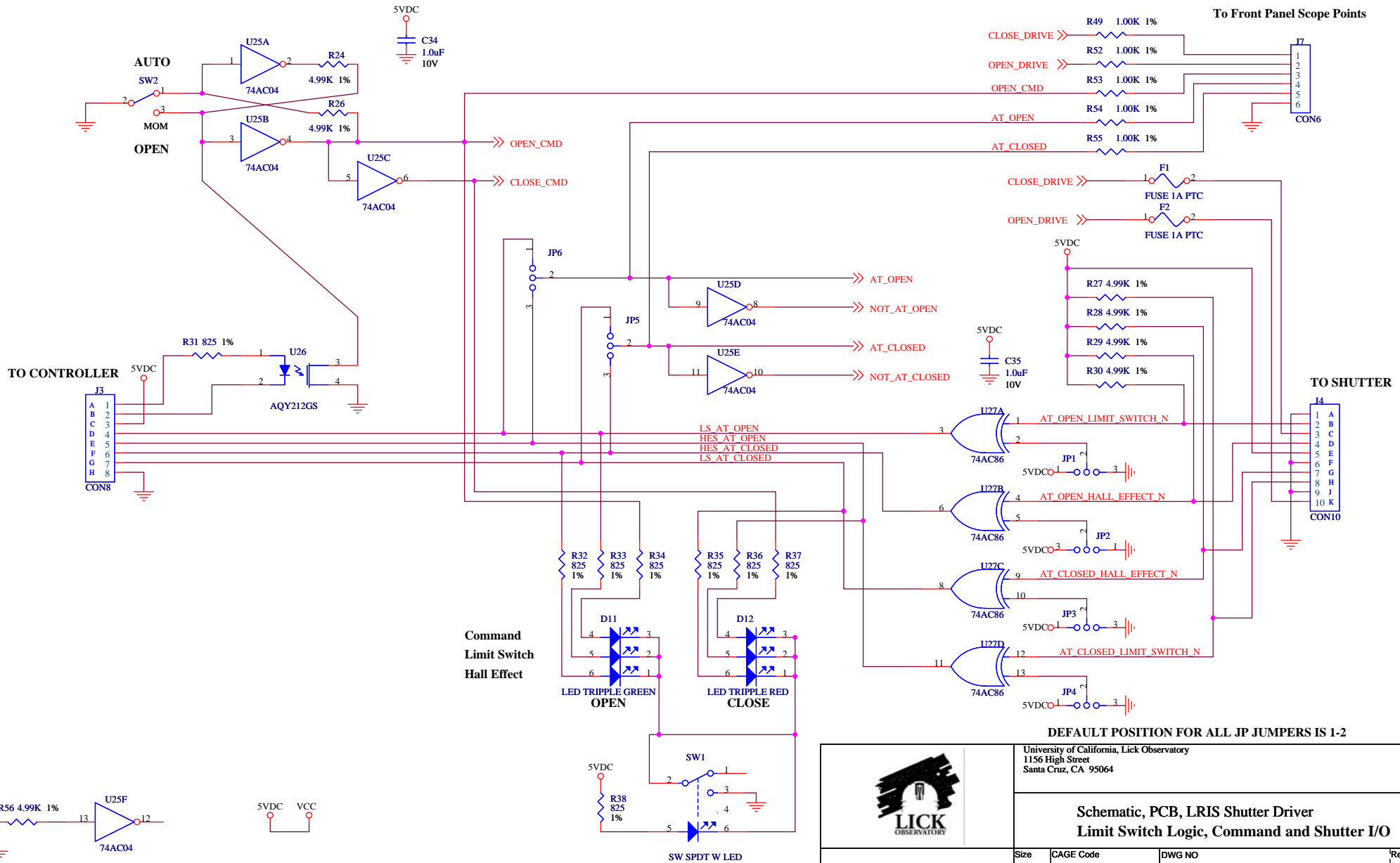


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**Schematic, PCB, LRIS Shutter Driver
 DC-DC Regulators, Drive Switches**

Size B	CAGE Code	DWG NO EL-4222	Rev 1.2
Scale		Sheet 3 of 6	

Wednesday, April 22, 2009



DEFAULT POSITION FOR ALL JP JUMPERS IS 1-2



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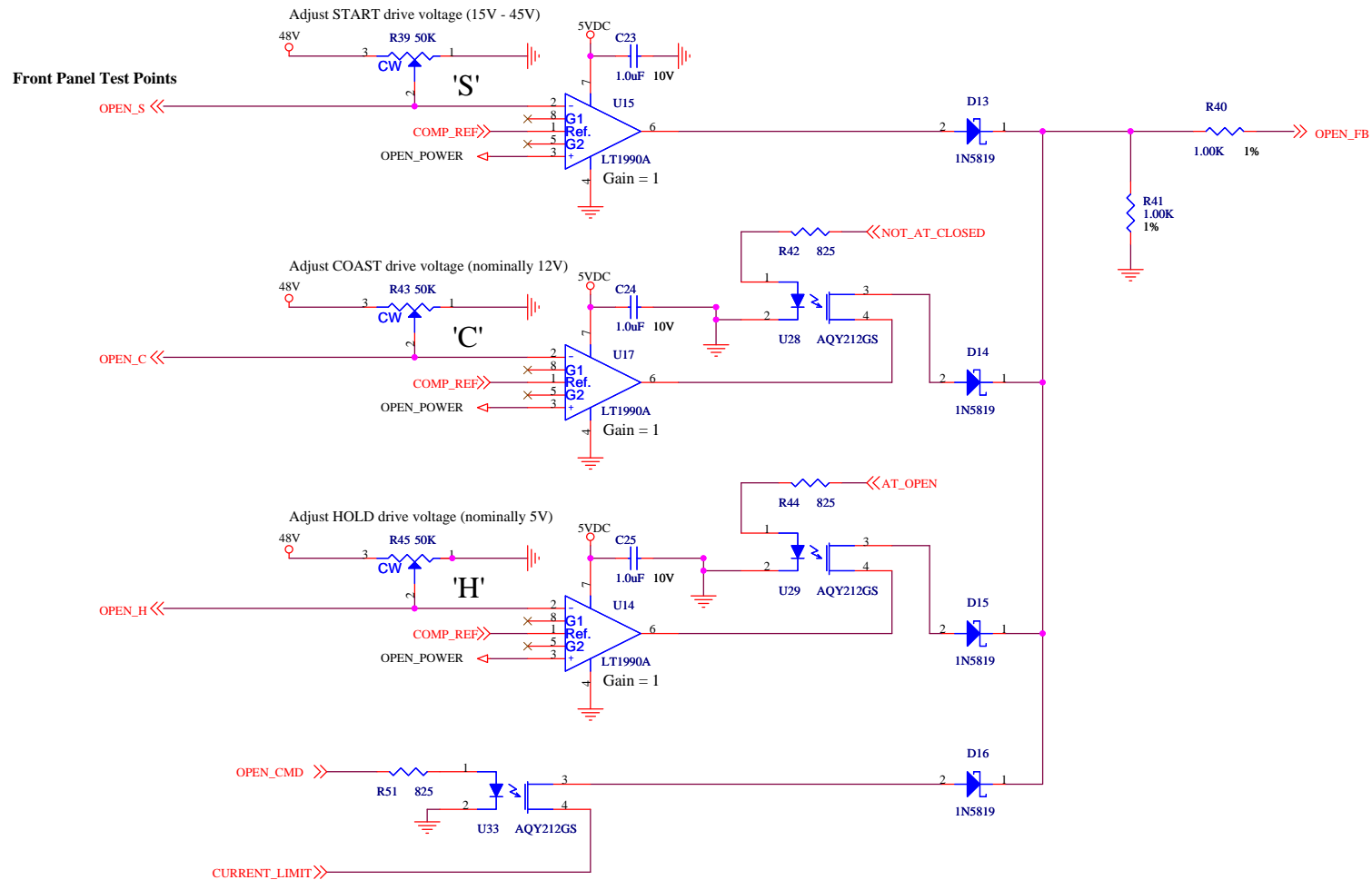
**Schematic, PCB, LRIS Shutter Driver
 Limit Switch Logic, Command and Shutter I/O**

Size B	CAGE Code	DWG NO	Rev 1.2
Scale		Sheet 4 of 6	

Wednesday, April 22, 2009

EL-4222

OPEN DRIVE SETTINGS



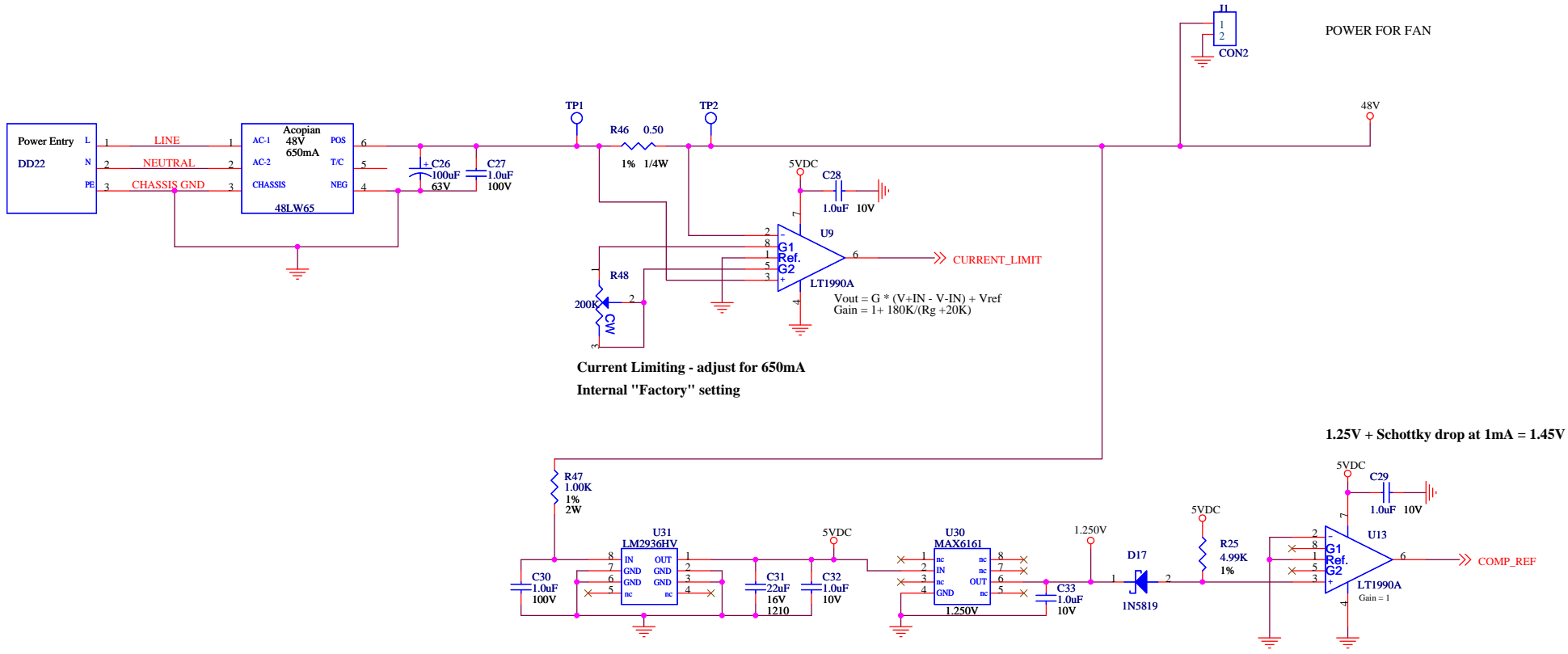
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Schematic, PCB, LRIS Shutter Driver Open Drive Settings

Size B	CAGE Code	DWG NO EL-4222	Rev 1.2
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Wednesday, April 22, 2009

Scale	Sheet 5 of 6
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	Schematic, PCB, LRIS Shutter Driver Power, Current Sense, Regulators		
Size B	CAGE Code	DWG NO EL-4222	Rev 1.2
Wednesday, April 22, 2009		Scale	Sheet 6 of 6