

CLOCK #1 37 PIN CONNECTOR

CLOCK GEN BOARD 1 AT: VME SLOT=14 DAC ADDR=8 SWITCH ADDR=2

PIN	NAME	CCD SIGNAL	CCD NUMBER	CODE	PIN	NAME
1	CLK0	PARALLEL 1	1	-	20	+12V
2	CLK1	PARALLEL 2	1	-	21	-12V
3	CLK2	PARALLEL 3	1	-	22	GND
4	CLK3	SUMMING WELL (A)	1	-	23	GND
5	CLK4	SERIAL PHASE #1 (A)	1	-	24	GND
6	CLK5	SERIAL PHASE #2 (A)	1	-	25	GND
7	CLK6	SERIAL PHASE #3	1	-	26	GND
8	CLK7	SERIAL PHASE #2 (B)	1	-	27	GND
9	CLK8	SERIAL PHASE #1 (B)	1	-	28	GND
10	CLK9	SUMMING WELL (B)	1	-	29	GND
11	CLK10	RESET GATE (A)	1	-	30	GND
12	CLK11	RESET GATE (B)	1	-	31	GND
13	CLK12	PARALLEL 1	2	-	32	GND
14	CLK13	PARALLEL 2	2	-		
15	CLK14	PARALLEL 3	2	-		
16	CLK15	SUMMING WELL (A)	2	-		
17	CLK16	SERIAL PHASE #1 (A)	2	-		
18	CLK17	SERIAL PHASE #2 (A)	2	-		
19	CLK18	SERIAL PHASE #3	2	-		
33	CLK19	SERIAL PHASE #2 (B)	2	-		
34	CLK20	SERIAL PHASE #1 (B)	2	-		
35	CLK21	SUMMING WELL (B)	2	-		
36	CLK22	RESET GATE (A)	2	-		
37	CLK23	RESET GATE (R)	2	-		

CLOCK #2 37 PIN CONNECTOR

CLOCK GEN BOARD 2 AT: VME SLOT=13 DAC ADDR=9 SWITCH ADDR=2

PIN	NAME	CCD SIGNAL	CCD NUMBER	CODE	PIN	NAME
1	CLK0	PARALLEL 1	3	-	20	+12V
2	CLK1	PARALLEL 2	3	-	21	-12V
3	CLK2	PARALLEL 3	3	-	22	GND
4	CLK3	SUMMING WELL (A)	3	-	23	GND
5	CLK4	SERIAL PHASE #1 (A)	3	-	24	GND
6	CLK5	SERIAL PHASE #2 (A)	3	-	25	GND
7	CLK6	SERIAL PHASE #3	3	-	26	GND
8	CLK7	SERIAL PHASE #2 (B)	3	-	27	GND
9	CLK8	SERIAL PHASE #1 (B)	3	-	28	GND
10	CLK9	SUMMING WELL (B)	3	-	29	GND
11	CLK10	RESET GATE (A)	3	-	30	GND
12	CLK11	RESET GATE (B)	3	-	31	GND
13	CLK12	PARALLEL 1	4	-	32	GND
14	CLK13	PARALLEL 2	4	-		
15	CLK14	PARALLEL 3	4	-		
16	CLK15	SUMMING WELL (A)	4	-		
17	CLK16	SERIAL PHASE #1 (A)	4	-		
18	CLK17	SERIAL PHASE #2 (A)	4	-		
19	CLK18	SERIAL PHASE #3	4	-		
33	CLK19	SERIAL PHASE #2 (B)	4	-		
34	CLK20	SERIAL PHASE #1 (B)	4	-		
35	CLK21	SUMMING WELL (B)	4	-		
36	CLK22	RESET GATE (A)	4	-		
37	CLK23	RESET GATE (B)	4	-		

REVISION

**UNIVERSITY OF CALIFORNIA
LICK OBSERVATORY**

**PIN ASSIGNMENTS
CLOCK BOARD OUTPUT CONNECTORS
HIRES SDSU2 CCD CONTROLLER**

DES'N BY: B. Alcott

ORIGIN DATE: 07-31-02

DWG. NO. NUM. 1 OF 1

DRAWN BY:

MODIFY DATE:

PATH: HIRES\CCD_UPGRADE\CLOCKCON

REV. A

EL-3422