

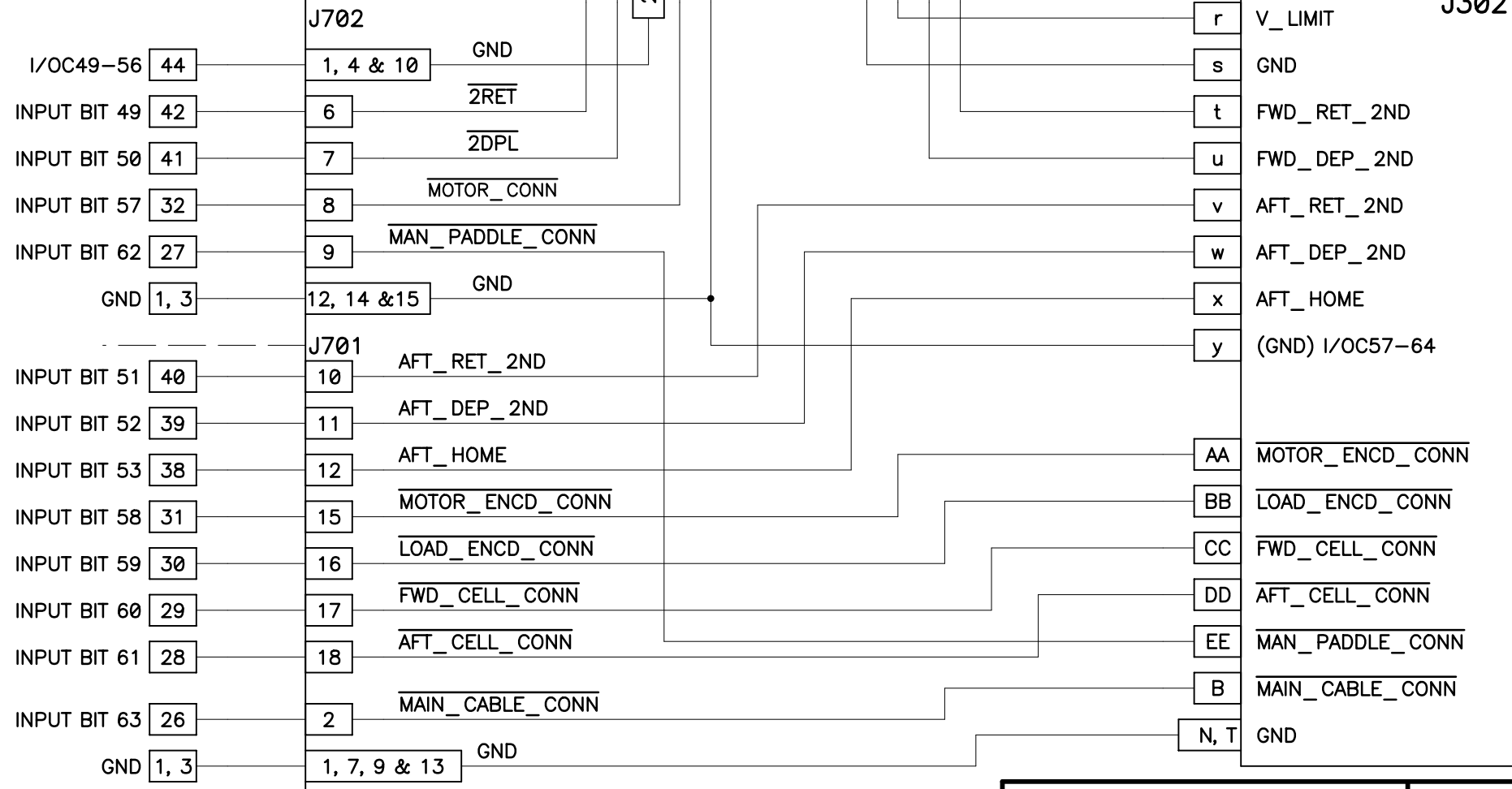
GALIL
DMC-2210
CONTROLLER

MAIN CONTROL

J1

GALIL
IOM-1964
(MOUNTED ON PANEL)

LIMIT INTERLOCK PANEL



REVISIONS

http://loel.ucolick.org/manual/ADC/schematics/ADC_power_sh_3.sch.pdf

04-16-04	ADDED PIN NUMBERS TO J202
04-20-04	RENUMBERED ALL CONNECTORS
04-22-04	REWORKED SECONDARY LIMIT SIGNALS
06-14-04	REARRANGED MOTOR_CONN CONNECTIONS AT J301 AND J404
02-07-05	ADDED CONNECTORS J701 AND J702 THAT LIVE ON I/O MOUNTING PANEL AND CORRECTED VARIOUS PIN NUMBERS
02-22-07	REWired MOTOR CABLE SO THAT THE CABLE HAS TWO 20 AWG WIRES IN PARALLEL TO REDUCE VOLTAGE DROP TO MOTOR - G. TOLLETH
03-12-07	CORRECTED LOCAL/REMOTE LABEL

UNIVERSITY OF CALIFORNIA LICK OBSERVATORY		ADC STAGE KECK ADC	
DES'N BY: B. Alcott	ORIGIN DATE: 09-16-03	DWG. NO.	NUM. 3 OF 6
Last SN	MODIFY DATE: 03-12-07	EL-3610	REV. A
PRINT TIME: 14:15:02	PRINT DATE: Mon Mar 12, 2007		
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