

ADC EXTENDED I/O BITS 17 – 48 HIGH CURRENT DRIVERS

BIT	PIN	I/O	SIGNAL	FUNCTION	TO
PWROUT17	104	0	-	-	-
PWROUT18	103	0	-	-	-
PWROUT19	102	0	-	-	-
PWROUT20	101	0	-	-	-
PWROUT21	100	0	-	-	-
PWROUT22	99	0	-	-	-
PWROUT23	98	0	-	-	-
PWROUT24	97	0	-	-	-

	96		I/OC17-24	+5V	-
	95		OUTC17-24	-	-

PWROUT25	84	0	-	+5V	-
PWROUT26	83	0	-	-	-
PWROUT27	82	0	-	-	-
PWROUT28	81	0	-	-	-
PWROUT29	80	0	-	-	-
PWROUT30	79	0	-	-	-
PWROUT31	78	0	-	-	-
PWROUT32	77	0	-	-	-

	76		I/OC25-32	-	-
	75		OUTC25-32	-	-

**HIGH CURRENT DRIVERS CAN BE USED BY BANKS OF 8
NORMAL I/O AND POWER OUTPUTS ARE MUTUALLY EXCLUSIVE**

- NOTES: 1) I/O BIT NUMBERS STARTING WITH A 'P' DENOTE POWER DRIVER ABILITY
 2) AS PER GALIL APP. NOTE #1438, TO USE BANKS 0 AND 1 FOR INPUTS, THE IOM-1964 CONNECTION LABELED OUTC_n MUST BE CONNECTED TO THE ISOLATED POWER SUPPLY GROUND.

REVISIONS

UNIVERSITY OF CALIFORNIA LICK OBSERVATORY		ADC EXTENDED I/O SIGNALS AND WIRING KECK ADC	
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